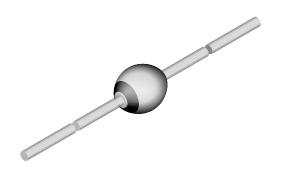


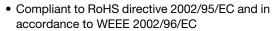
Vishay Semiconductors

Fast Avalanche Sinterglass Diode



FEATURES

- Glass passivated junction
- · Hermetically sealed package



Halogen-free according to IEC 61249-2-21 definition



RoHS

COMPLIANT HALOGEN FREE

APPLICATIONS

• High voltage fast rectification diode

MECHANICAL DATA

Case: SOD-57

Terminals: plated axial leads, solderable per MIL-STD-750,

method 2026

Polarity: color band denotes cathode end

Mounting position: any **Weight:** approx. 369 mg

PARTS TABLE					
PART	TYPE DIFFERENTIATION	PACKAGE			
BY268	$V_R = 1400 \text{ V}; I_{FAV} = 0.8 \text{ A}$	SOD-57			
BY269	V _R = 1600 V; I _{FAV} = 0.8 A	SOD-57			

949539

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	PART	SYMBOL	VALUE	UNIT
Peak reverse voltage, non repetitive		BY268	V _{RSM}	1600	V
		BY269	V _{RSM}	1800	V
Reverse voltage	See electrical characteristics	BY268	V _R	1400	V
		BY269	V _R	1600	V
Peak forward surge current	t _p = 10 ms, half sine wave		I _{FSM}	20	Α
Average forward current			I _{FAV}	0.8	Α
Non repetitive reverse avalanche energy	I _{(BR)R} = 0.4 A		E _R	10	mJ
Junction and storage temperature range			$T_j = T_{stg}$	- 55 to + 175	°C

MAXIMUM THERMAL RESISTANCE (T _{amb} = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Junction ambient	Lead length I = 10 mm, T _L = constant	R_{thJA}	45	K/W	
	On PC board with spacing 25 mm	R_{thJA}	100	K/W	

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ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	I _F = 0.4 A		V _F	-	-	1.25	V
Reverse current	V _R = 1400 V	BY268	I _R	-	1	2	μΑ
	V _R = 1600 V	BY269	I _R	-	1	2	μΑ
	V _R = 1400 V, T _j = 100 °C	BY268	I _R	-	-	15	μΑ
	V_{R} = 1600 V, T_{j} = 100 °C	BY269	I _R	-	-	15	μΑ
Reverse recovery time	I _F = 0.5 A, I _R = 1 A, i _R = 0.25 A		t _{rr}	-	-	400	ns

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

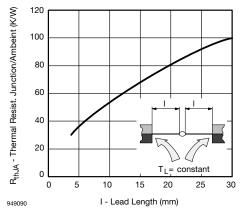


Fig. 1 - Max. Thermal Resistance vs. Lead Length

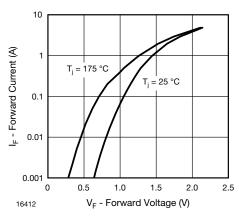


Fig. 2 - Max. Forward Current vs. Forward Voltage

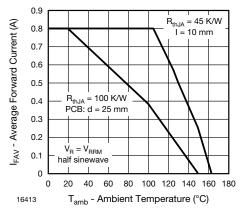


Fig. 3 - Max. Average Forward Current vs. Ambient Temperature

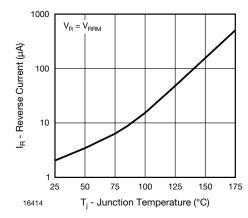


Fig. 4 - Max. Reverse Current vs. Junction Temperature



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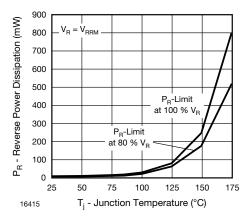


Fig. 5 - Max. Reverse Power Dissipation vs. Junction Temperature

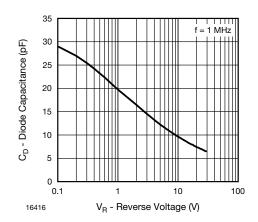
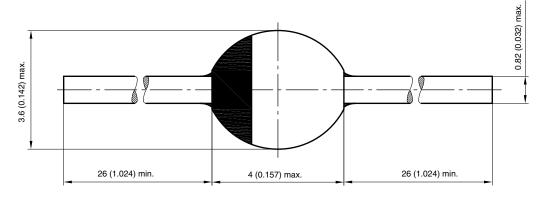


Fig. 6 - Diode Capacitance vs. Reverse Voltage

PACKAGE DIMENSIONS in millimeters (inches): SOD-57



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